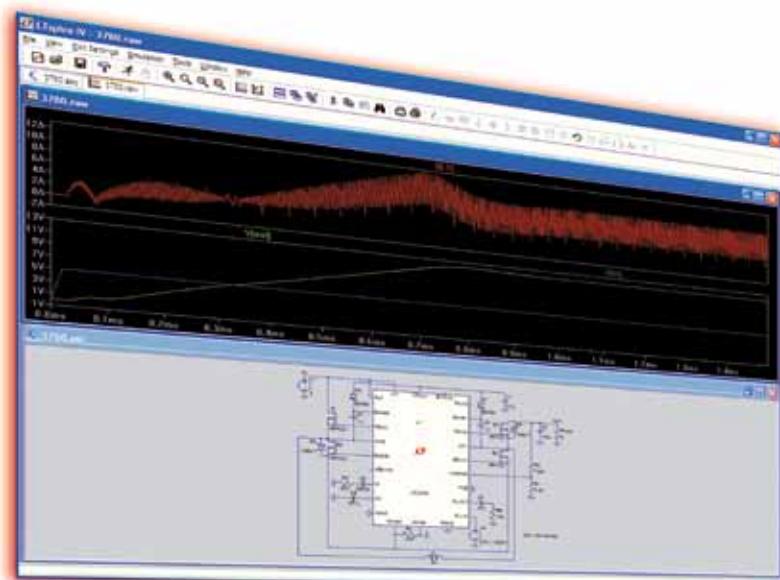


# LTspice

## What you can do with the free LTspice simulation software:



- Do simulations of SMPS and other analog circuitry
- Use one of the 100s of demo circuits available on [www.linear.com/democircuits](http://www.linear.com/democircuits)
- Use a pre-drafted test fixture (JIG)
- Test models during development
- Get draft starting points
- Use the schematic editor to create your own design
- Add a macromodel & opening test fixture
- Get the latest datasheet
- Add sources, loads & additional circuit elements
- Run and edit simulations
- Run and probe a circuit using waveform viewer and test fixtures
- Plot plans, calculate frequencies and power dissipation
- Generate bill of materials and efficiency reports
- Benefit from a Built-in Help system and an independent LTspice® user's group with many tutorials, libraries and examples



Linear Technology produces power management, data conversion, signal conditioning, RF and interface ICs, and  $\mu$ Module® subsystems across a large range of markets.



[www.linear.com/ltspice](http://www.linear.com/ltspice)



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### Würth Elektronik component library for LTspice

Get all passive components of Würth Elektronik in one library.  
Upload it in LTspice and start designing.

Check out more software tools on  
[www.we-online.com/toolbox](http://www.we-online.com/toolbox)

The toolbox offers various design and select tools for passive and electromechanical components, such as Component Selector, Application Guide, Product Trainings, Parameter Search, etc.



# LTspice

## LTspice Keyboard Shortcuts

LTspice HotKeys					Simulator Directives - Dot Commands	
	Schematic	Symbol	Waveform	Netlist	Command	Short Description
Modes	ESC - Exit Mode	ESC - Exit Mode			.AC	Perform a Small Signal AC Analysis
	F3 - Draw Wire				.BACKANNO	Annotate the Subcircuit Pin Names on Port currents
	F5 - Delete	F5 - Delete	F5 - Delete		.DC	Perform a DC Source Sweep Analysis
	F6 - Duplicate	F6 - Duplicate			.END	End of Netlist
	F7 - Move	F7 - Move			.ENDS	End of Subcircuit Definition
	F8 - Drag	F8 - Drag			.FOUR	Compute a Fourier Component
	F9 - Undo	F9 - Undo	F9 - Undo	F9 - Undo	.FUNC	User Defined Functions
	Shift+F9 - Redo	Shift+F9 - Redo	Shift+F9 - Redo	Shift+F9 - Redo	.FERRET	Download a File Given the URL
View	Ctrl+Z - Zoom Area	Ctrl+Z - Zoom Area	Ctrl+Z - Zoom Area		.GLOBAL	Declare Global Nodes
	Ctrl+B - Zoom Back	Ctrl+B - Zoom Back	Ctrl+B - Zoom Back		.IC	Set Initial Conditions
	Space - Zoom Fit		Ctrl+E - Zoom Extents		.INCLUDE	Include another File
	Ctrl+G - Toggle Grid		Ctrl+G - Toggle Grid	Ctrl+G - Goto Line #	.LIB	Include a Library
	U - Mark Uncon. Pins	Ctrl+W - Attribute Window	'0' - Clear		.LOADBIAS	Load a Previously Solved DC Solution
	A - Mark Text Anchors	Ctrl+A - Attribute Editor	Ctrl+A - Add Trace		.MEASURE	Evaluate User-Defined Electrical Quantities
	Alt+Click - Power		Ctrl+Y - Vertical Autorange	Ctrl+R - Run Simulation	.MODEL	Define a SPICE Model
	Ctrl+Click - Attr. Edit		Ctrl+Click - Average		.NET	Compute Network Parameters in a .AC Analysis
Place	Ctrl+H - Halt Simulation		Ctrl+H - Halt Simulation	Ctrl+H - Halt Simulation	.NODESET	Supply Hints for Initial DC Solution
	R - Resistor	R - Rectangle	Command Line Switches		.NOISE	Perform a Noise Analysis
	C - Capacitor	C - Circle	Flag	Short Description	.OP	Find the DC Operating Point
	L - Inductor	L - Line	-ascii	Use ASCII .raw files. (Degrades performance!)	.OPTIONS	Set Simulator Options
	D - Diode	A - Arc	-b	Run in batch mode.	.PARAM	User-Defined Parameters
	G - GND		-big or -max	Start as a maximized window.	.SAVE	Limit the Quantity of Saved Data
	S - Spice Directive		-encrypt	Encrypt a model library.	.SAVEBIAS	Save Operating Point to Disk
	T - Text	T - Text	-FastAccess	Convert a binary .raw file to Fast Access Format.	.STEP	Parameter Sweeps
			-netlist	Convert a schematic to a netlist.	.SUBCKT	Define a Subcircuit
			-nowine	Prevent use of WINE(Linux) workarounds.	.TEMP	Temperature Sweeps
			-PCBnetlist	Convert a schematic to a PCB netlist.	.TF	Find the DC Small-Signal Transfer Function
			-registry	Store user preferences in the registry.	.TRAN	Do a Nonlinear Transient Analysis
			-Run	Start simulating the schematic on open.	.WAVE	Write Selected Nodes to a .WAV file
			-SOI	Allow MOSFET's to have up to 7 nodes in subcircuit.		
			-uninstall	Executes one step of the uninstallation process.		
			-wine	Force use of WINE(Linux) workarounds.		
Suffix		Suffix		Constants		
		f	1e-15	E	2.7182818284590452354	
T	1e12	p	1e-12	Pi	3.14159265358979323846	
G	1e9	n	1e-9	K	1.3806503e-23	
Meg	1e6	u	1e-6	Q	1.602176462e-19	
K	1e3	M	1e-3	TRUE	1	
		Mil	25.4e-6	FALSE	0	